Integrated Redundancy Architecture and Method for Providing Redundancy Allocation to an Embedded Memory System BUR920030151US1
Ellis et al.

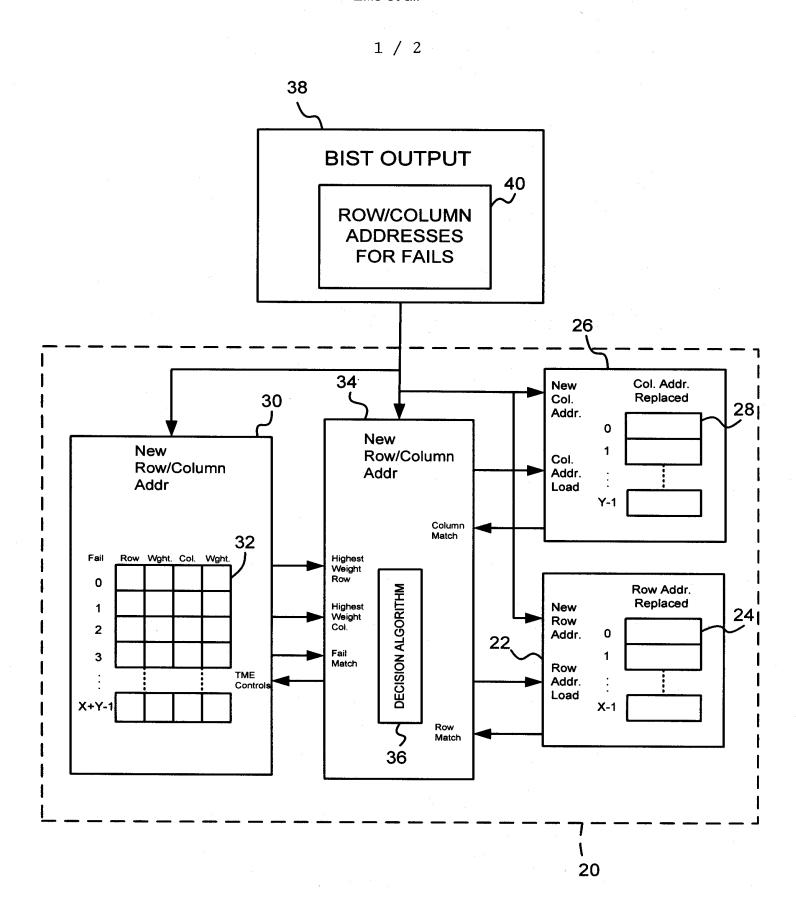


FIG. 1

